

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,038,481 B2
APPLICATION NO. : 10/807703
DATED : May 2, 2006
INVENTOR(S) : Kenneth W. Marr

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the specification:

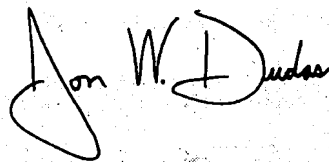
COLUMN 1, LINE 31,	change “IC,” “device” ” to --“IC,” “device”--
COLUMN 4, LINE 34,	change “failure rate r(t)” to --failure rate f(t)--
COLUMN 6, LINE 10,	change “t ₀ may” to --t ₀ may--
COLUMN 8, LINE 16,	change “burn-in circuitry.” to --burn-in circuitry 650.--
COLUMN 8, LINE 47,	change “example, a 4” to --example, a--
COLUMN 8, LINE 48,	change “Mb” to --4Mb--
COLUMN 8, LINE 49,	change “144xcolumn-elements” to --144x4 column-elements--
COLUMN 10, LINE 1,	change “IC dice” to --IC die--

In the claims:

CLAIM 15, COLUMN 12, LINE 12,	change “removing sacrificial” to --removing the sacrificial--
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Signed and Sealed this

Thirteenth Day of November, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office